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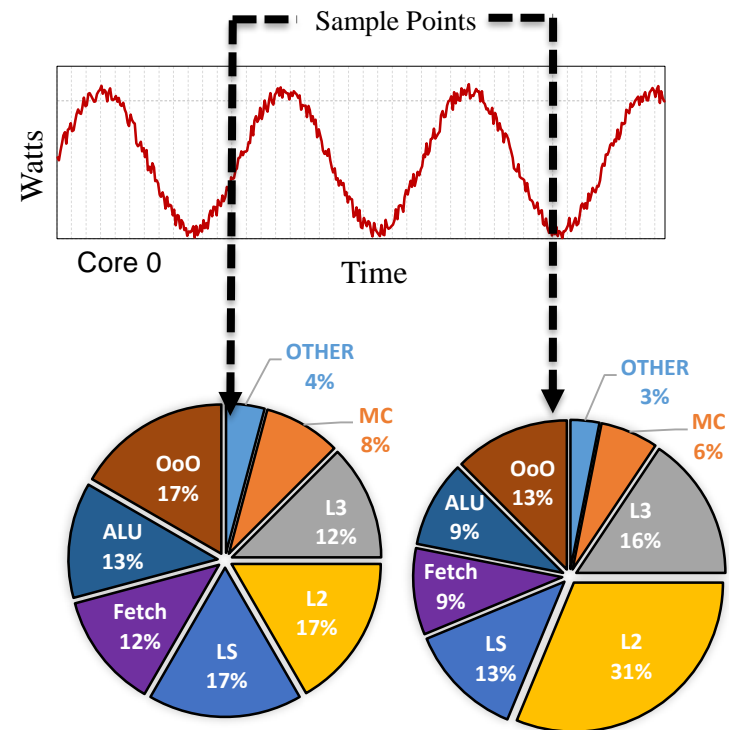
SBAC-PAD 2015

WattWatcher: Fine-Grained Power Estimation for Emerging Workloads

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Motivation

- **Understanding power at a fine-granularity is still a challenge**
 - Thermal effects, DVFS policies
- **Not always easy for researchers**
- **Simple and detailed power estimation is extremely useful**
- **Some methods currently available...**



Currently Available Methods

- **Direct Measurements [1]**
 - Hardware probes
- **Curve Fitting [2,3,4,5,6]**
 - Machine learning models
- **Power PMCs [6,7]**
 - E.g. Intel RAPL
- **Simulators[9,10,11,12]**
 - E.g. McPAT plugins to simulation environment

Design Space

- **Diverse design space to explore (subjective taxonomy)**

	Accuracy	Detail	Frequency	Cost (\$)	Speed
Direct Measurements	++	-	~us-ms	-	Fast
Power PMCs	+	-	~ms	=	Fast
Curve Fitting	=	=	~us-s	+	Fast/Offline Training
Simulators	+	+	~ns	+	Slow

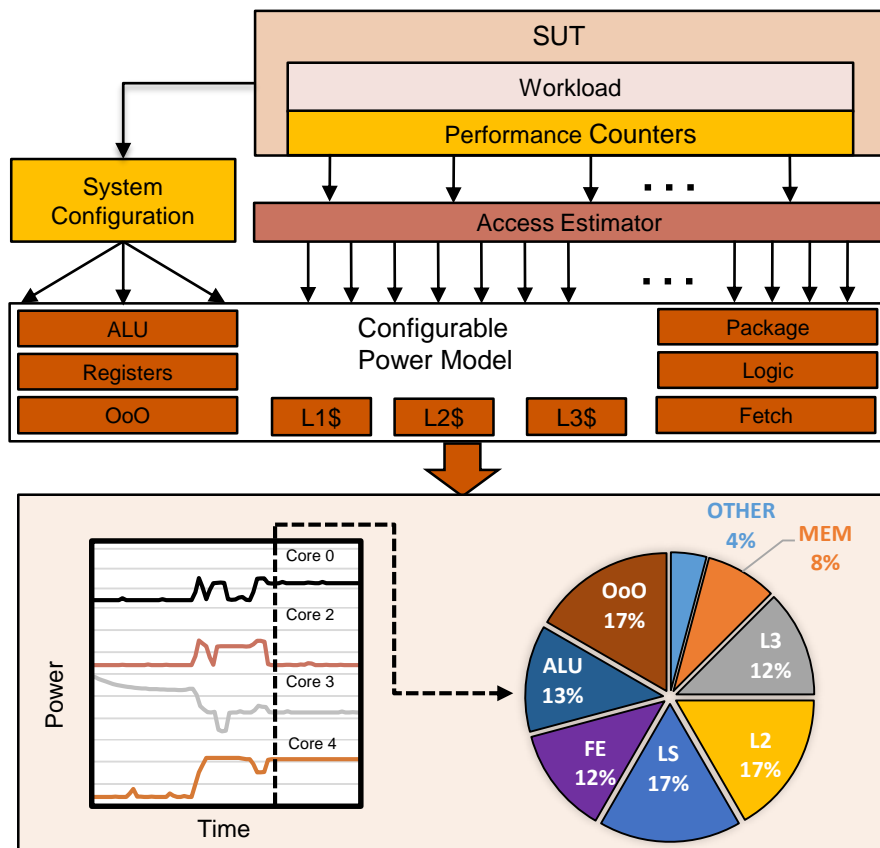
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WattWatcher	+	+	~ms	+	Fast

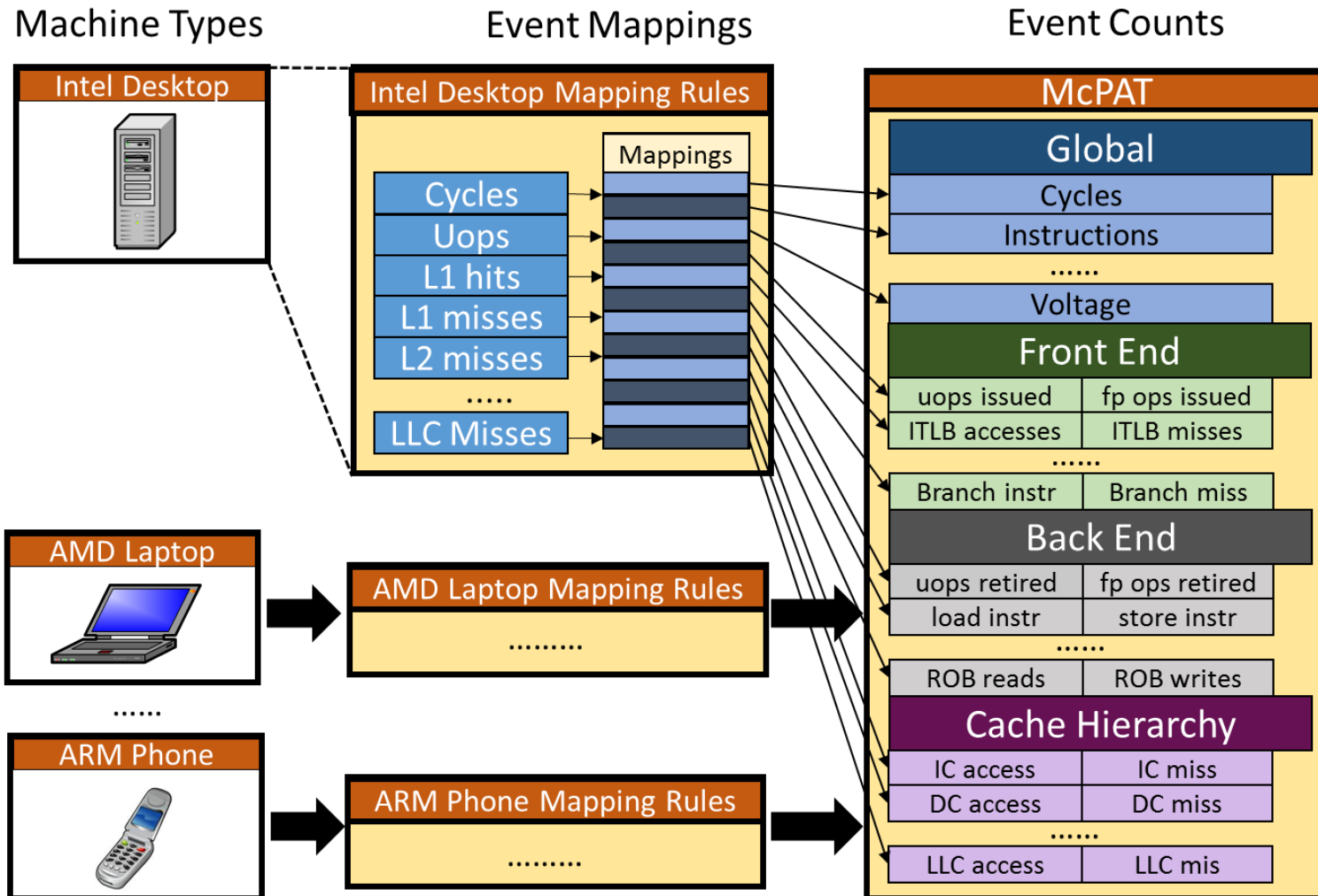
- **WattWatcher offers functional-unit power breakdowns in real-time, on real hardware**

WattWatcher Overview



- **Online / Real-Time**
- **MCPAT-based**
- **Configurable**
- **Low Overhead**

WattWatcher Overview

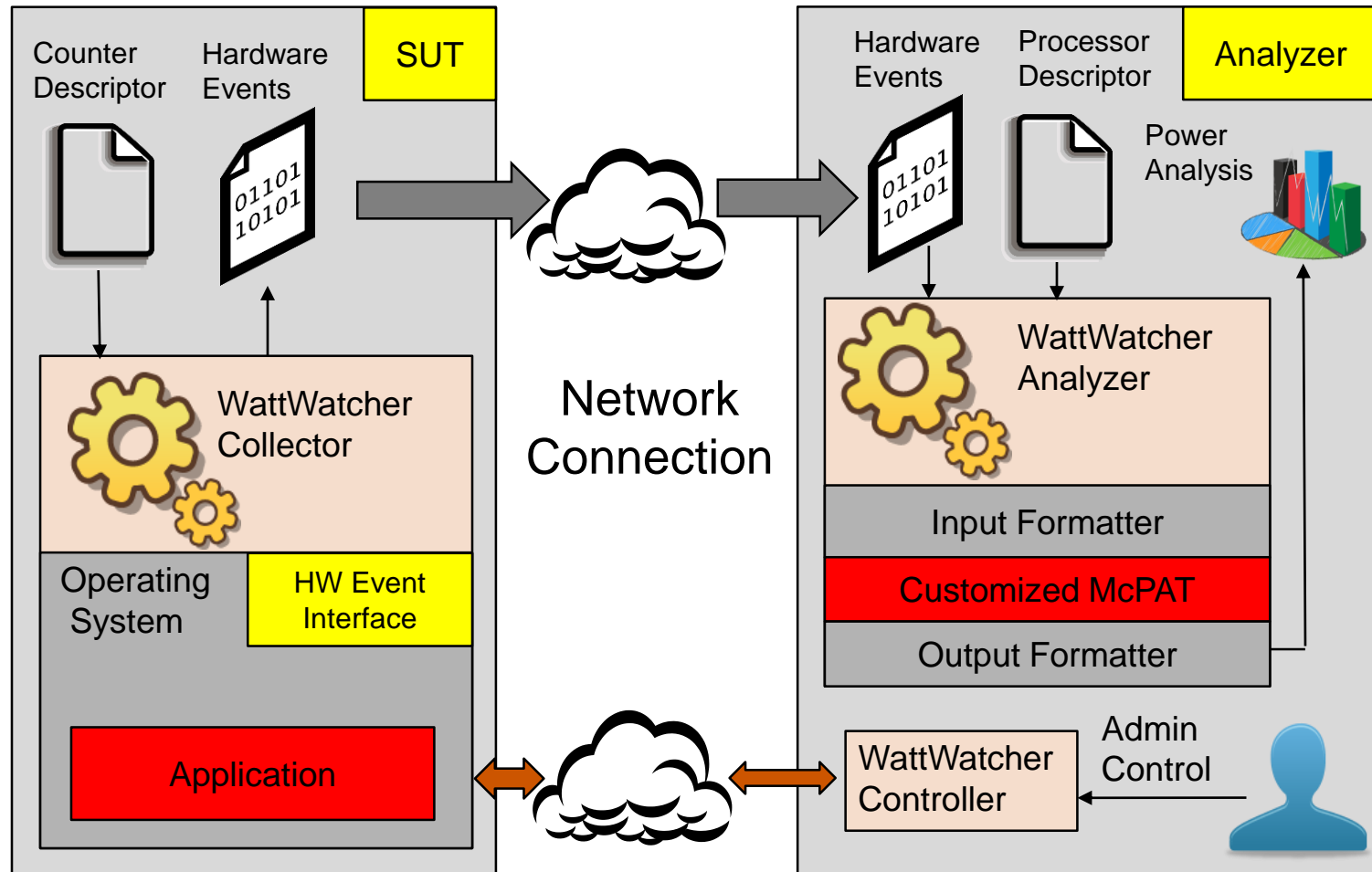


WattWatcher Hardware Events

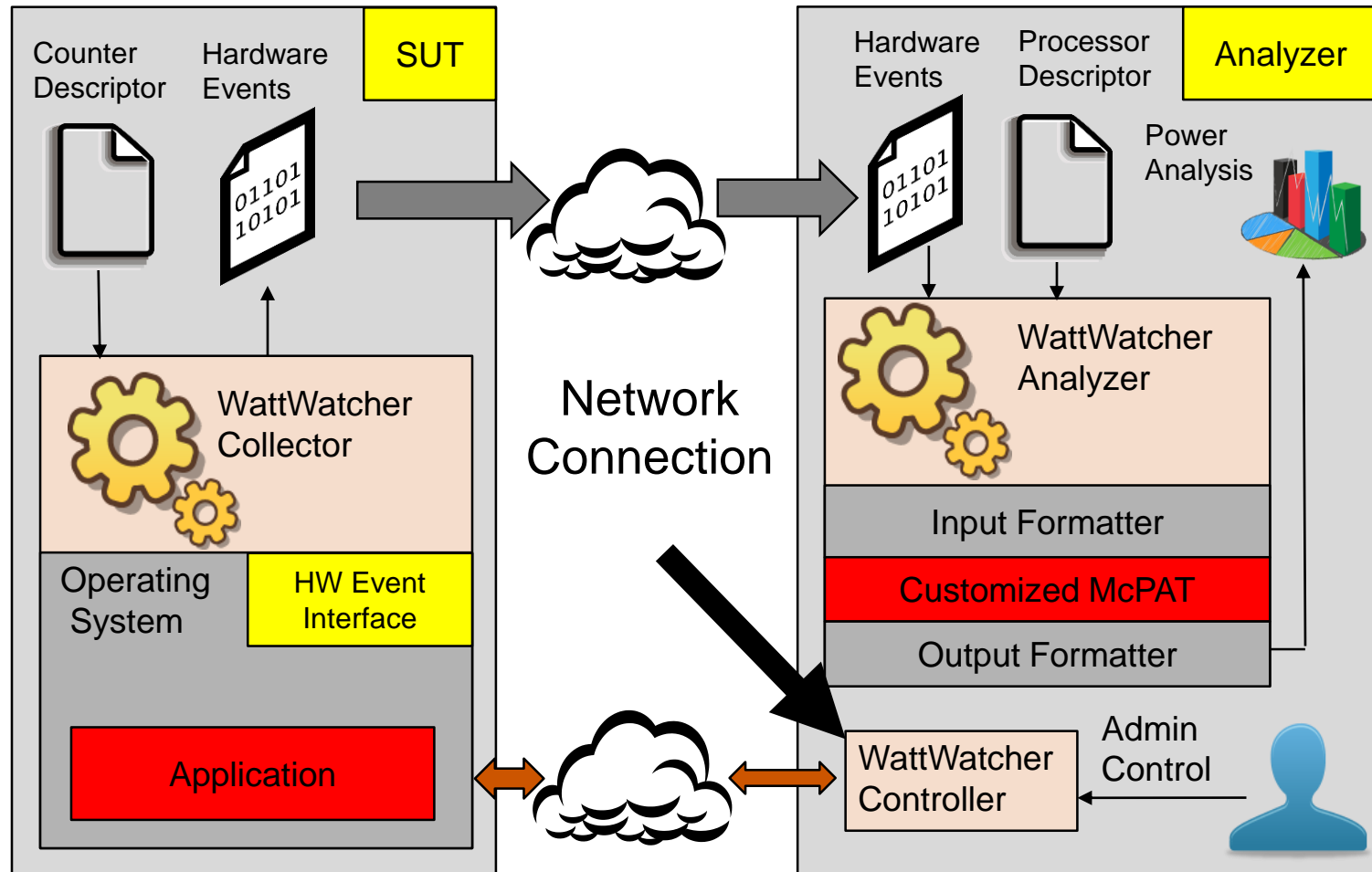
Category	Hardware Event
General	Context Switches Frequency Voltage Cycles
Frontend	Branch Mispredictions IC Misses iTLB Misses uops Issued
LS/Caches	L1 Misses/Hits L2 Misses LLC Misses dTLB Misses
Execution	FP Scalar FP Packed FP Width
Retirement	Uops Retired

- **Hardware performance counters feed McPAT**
- **Some low-level McPAT events unavailable from counters**
- **Unavailable statistics estimated from available counters**

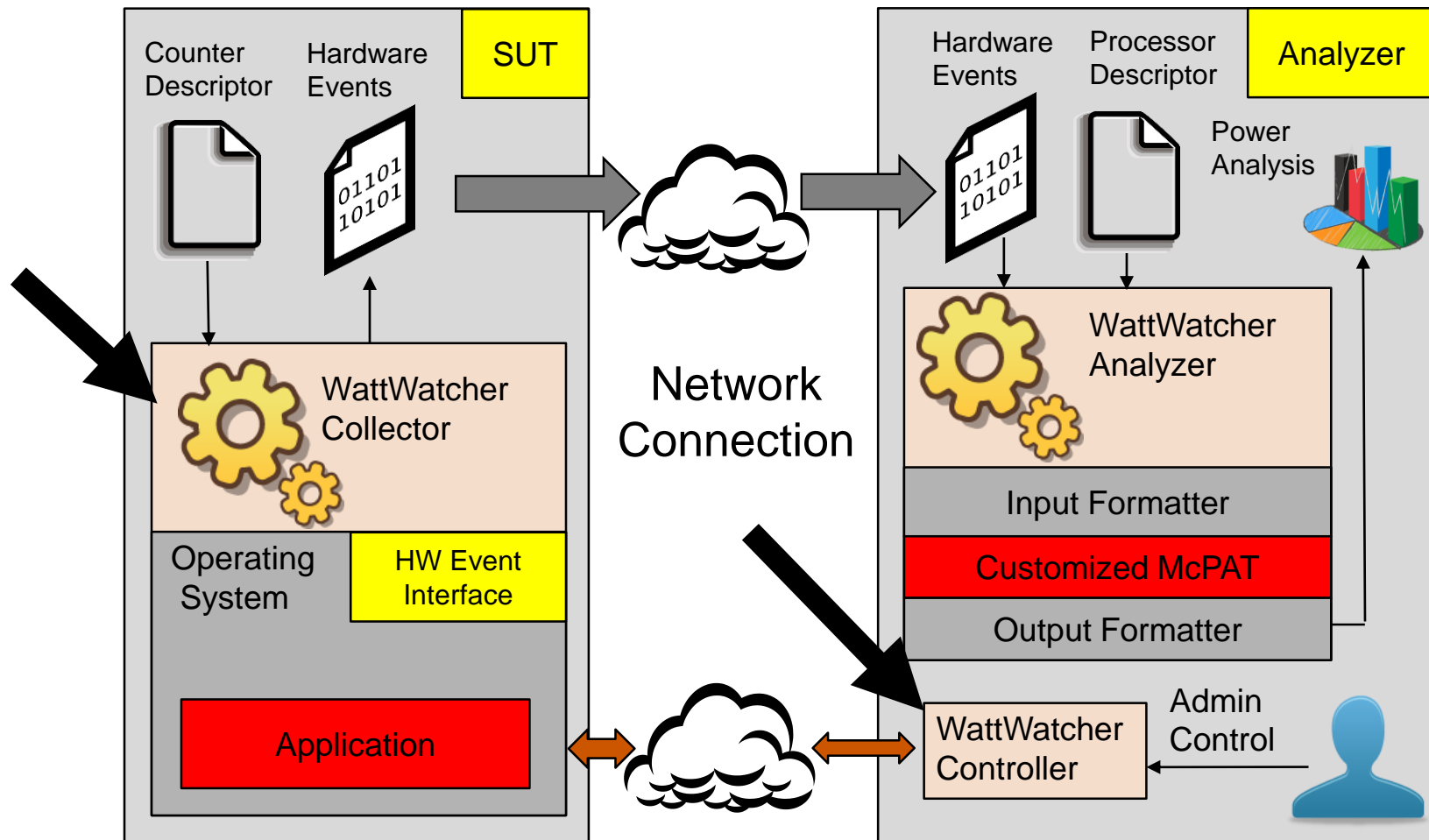
WattWatcher Toolkit Overview



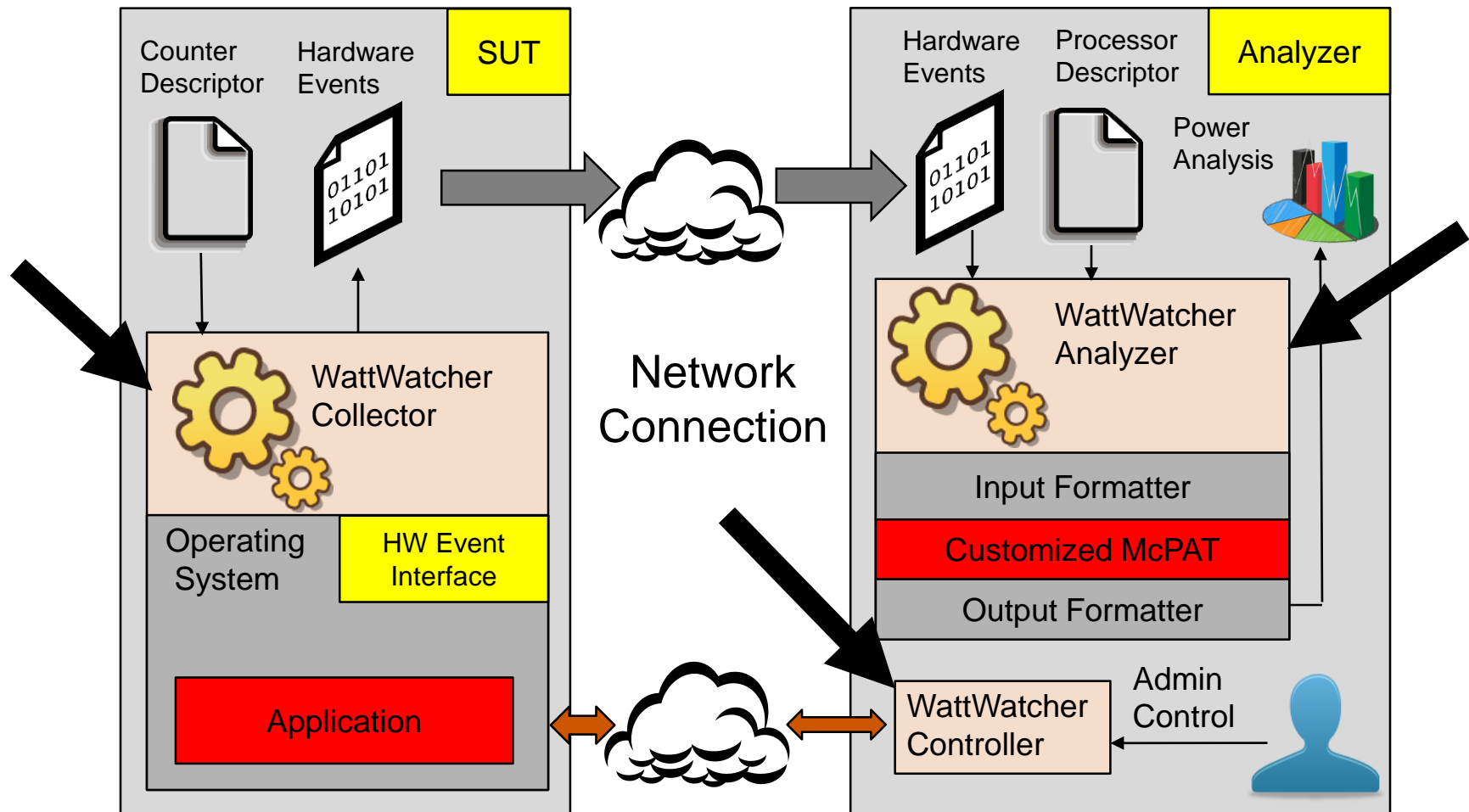
WattWatcher Toolkit Overview



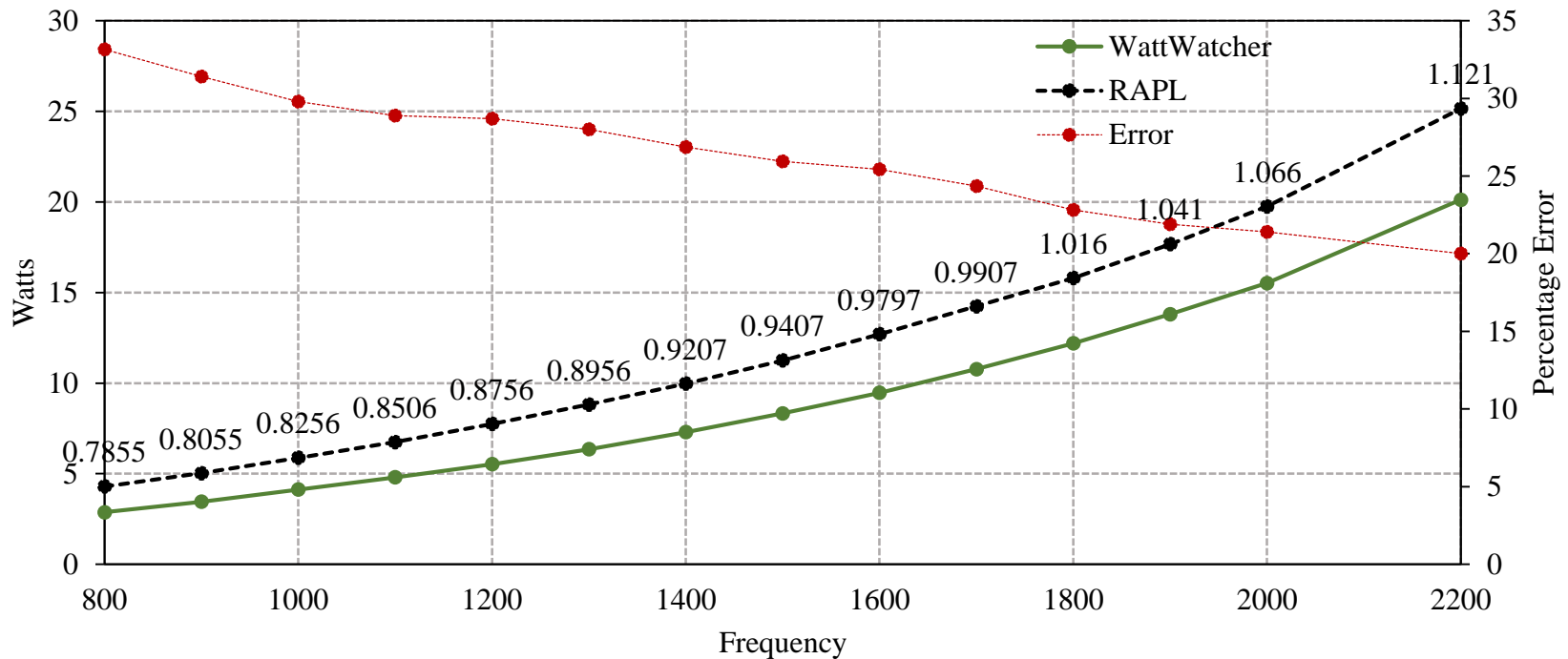
WattWatcher Toolkit Overview



WattWatcher Toolkit Overview



WattWatcher Calibration



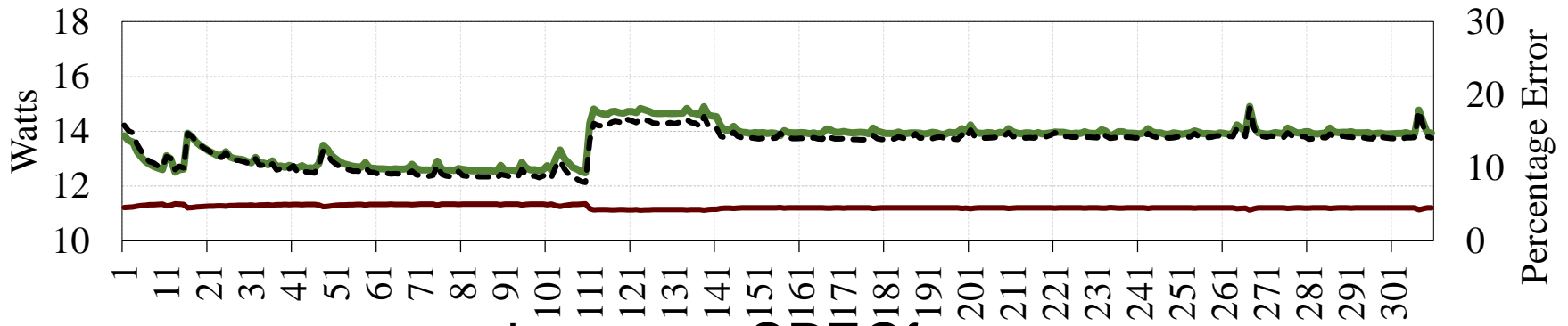
- **McPAT typically underestimates power [10]**
- **Small amount of course-grained calibration required**
- **More sophisticated corrections are available [13]**

Verification

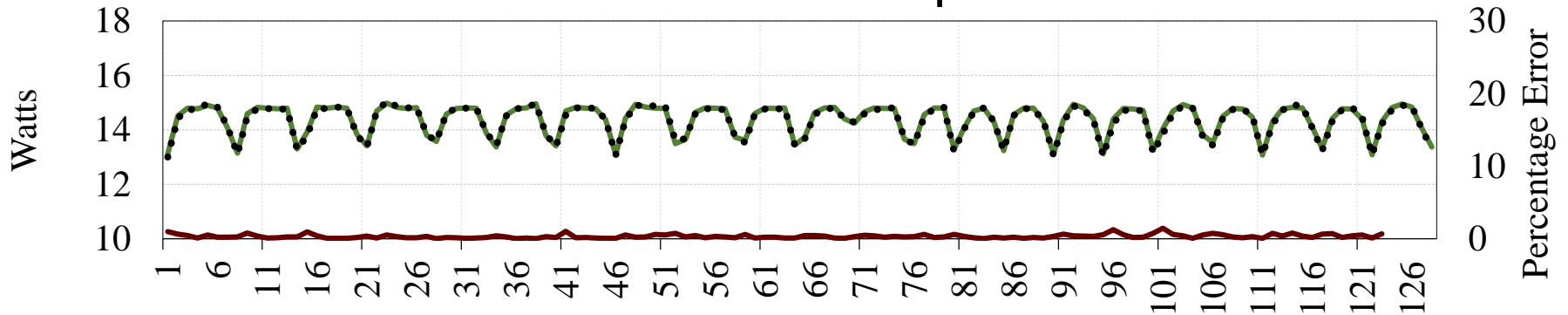
- **Intel Sandy Bridge Laptop**
 - Intel i7 2720QM
 - 32nm Process
 - 45W TDP
- **Workloads: SPECFP + SPECINT[11], PARSEC[12]**
- **Compared against RAPL counters**
- **Other SUTs: Intel Haswell and AMD Piledriver**
- **All results use previous coarse-grained calibration**

Verification

xalancbmk : SPECint

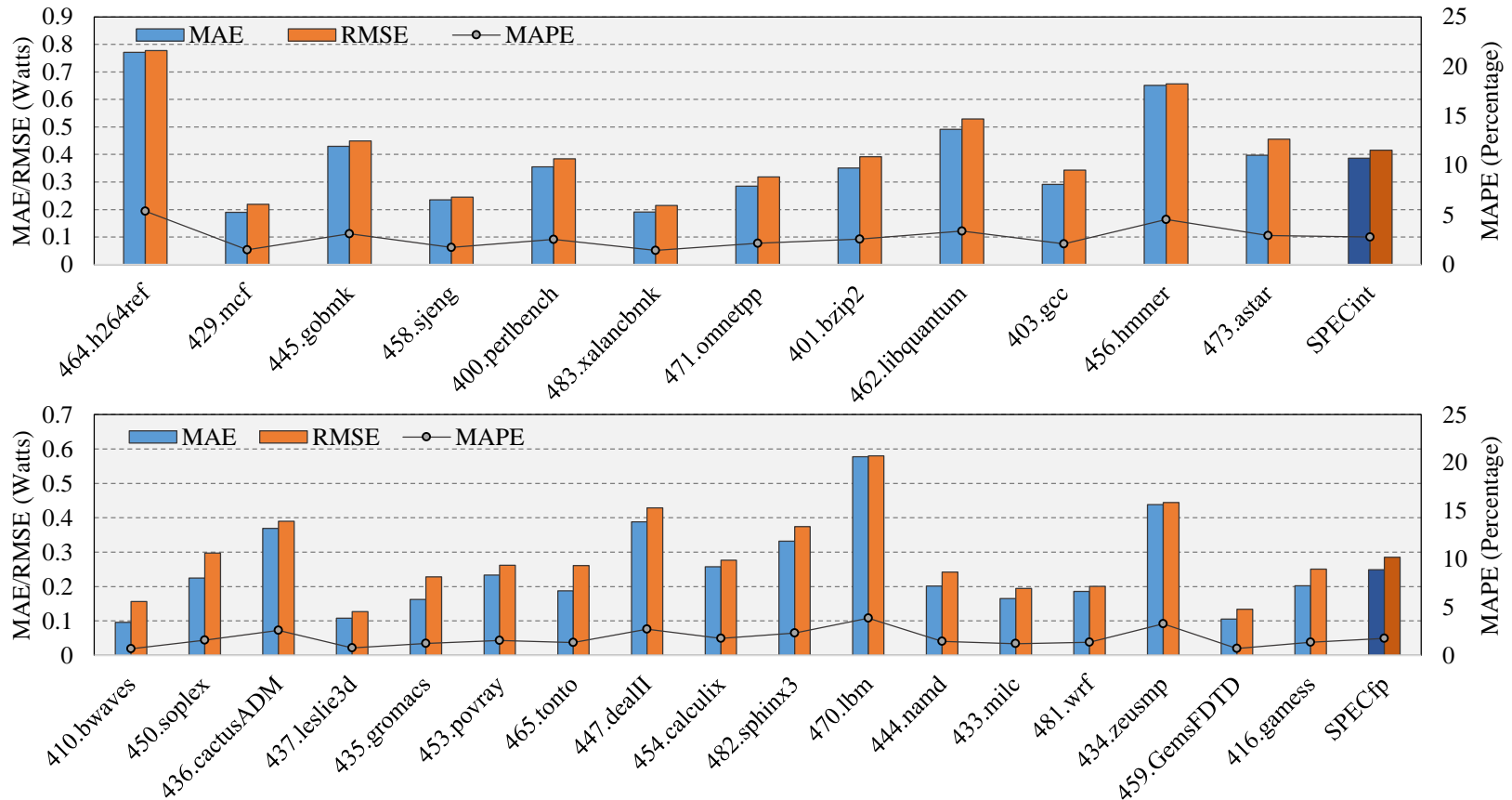


bwaves : SPECfp



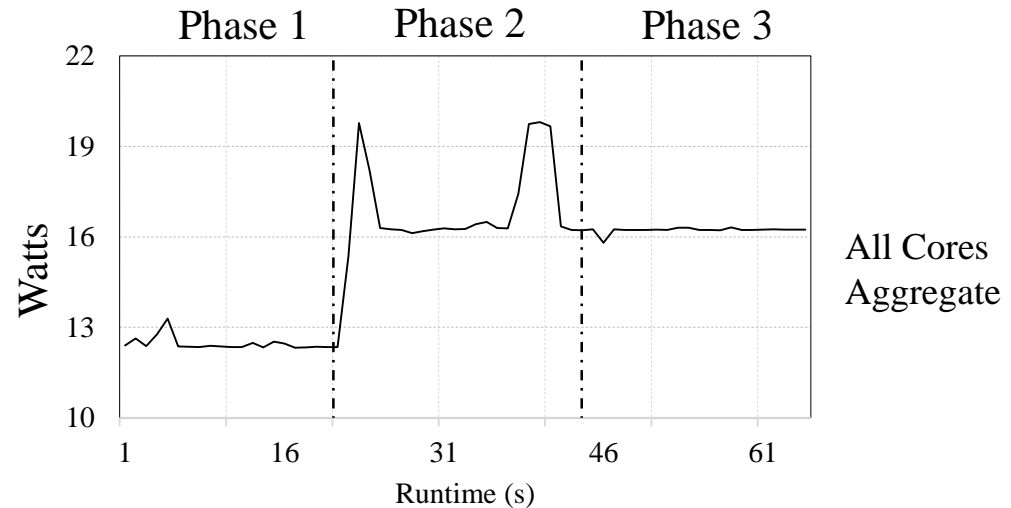
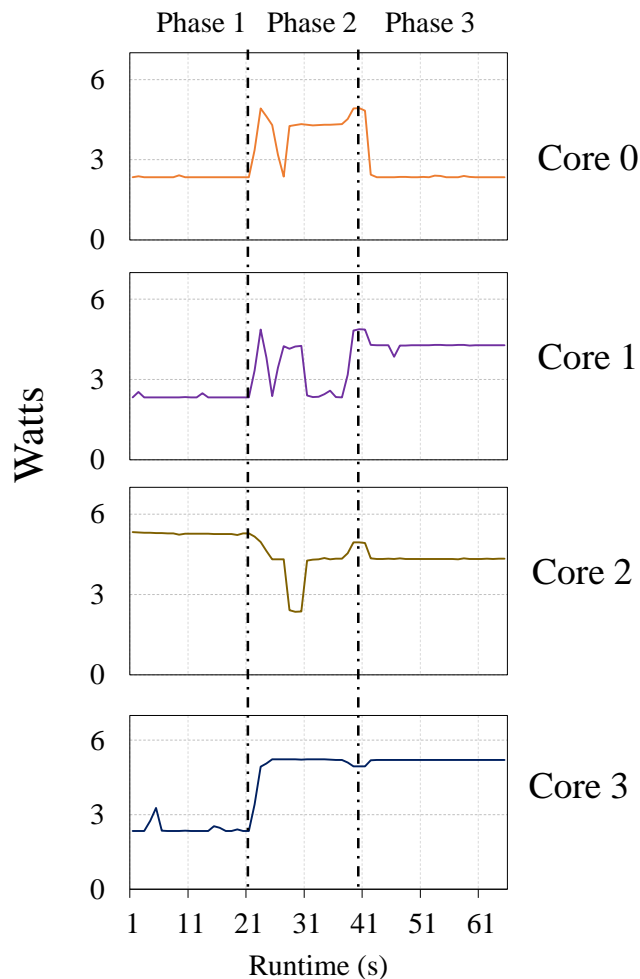
- **Pearson correlation coefficient (0.982, 0.995)**

Verification



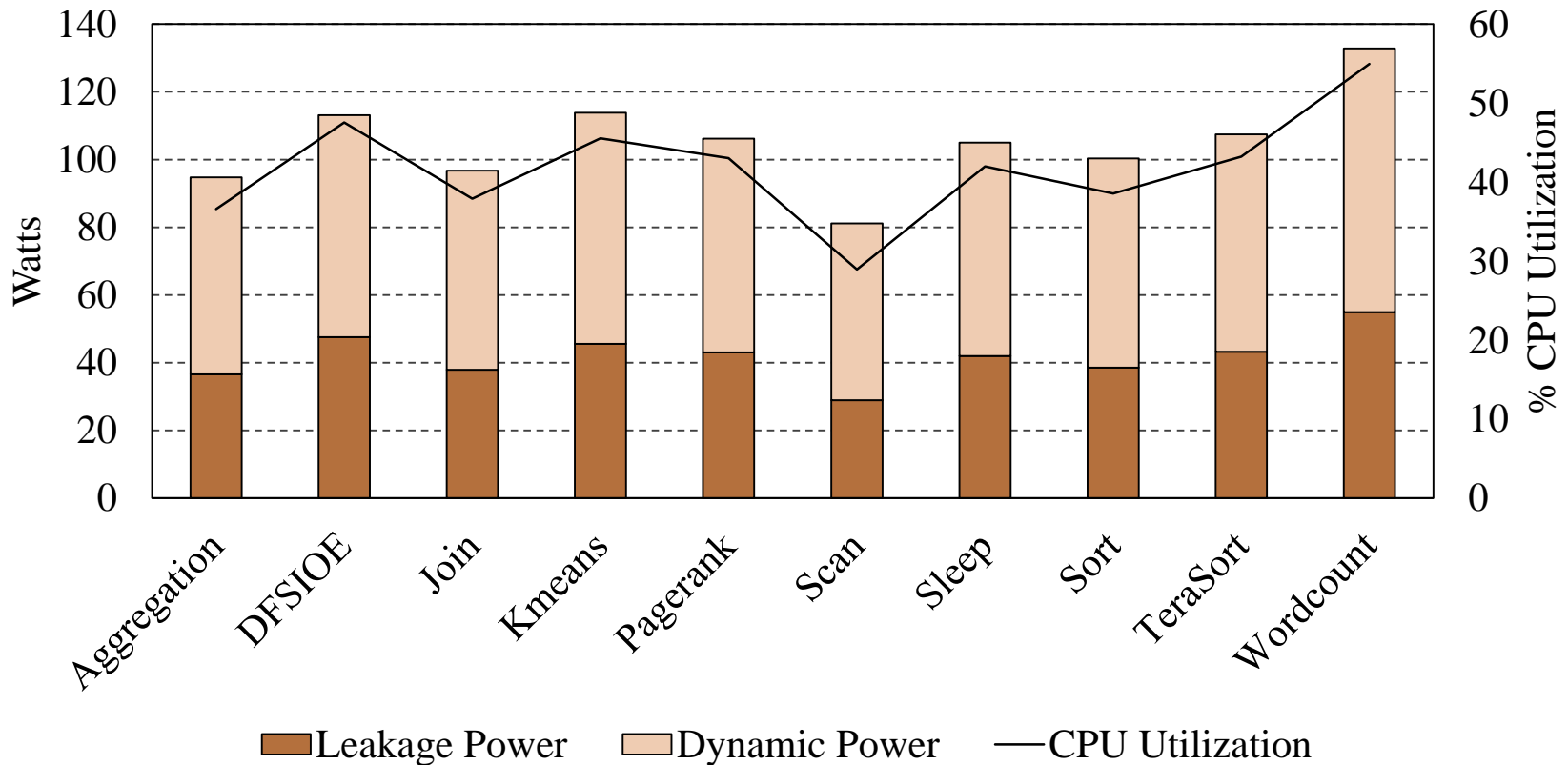
- **MAPE over all workloads is 2.67%**

Case Studies 1: Per Core Power Measurements



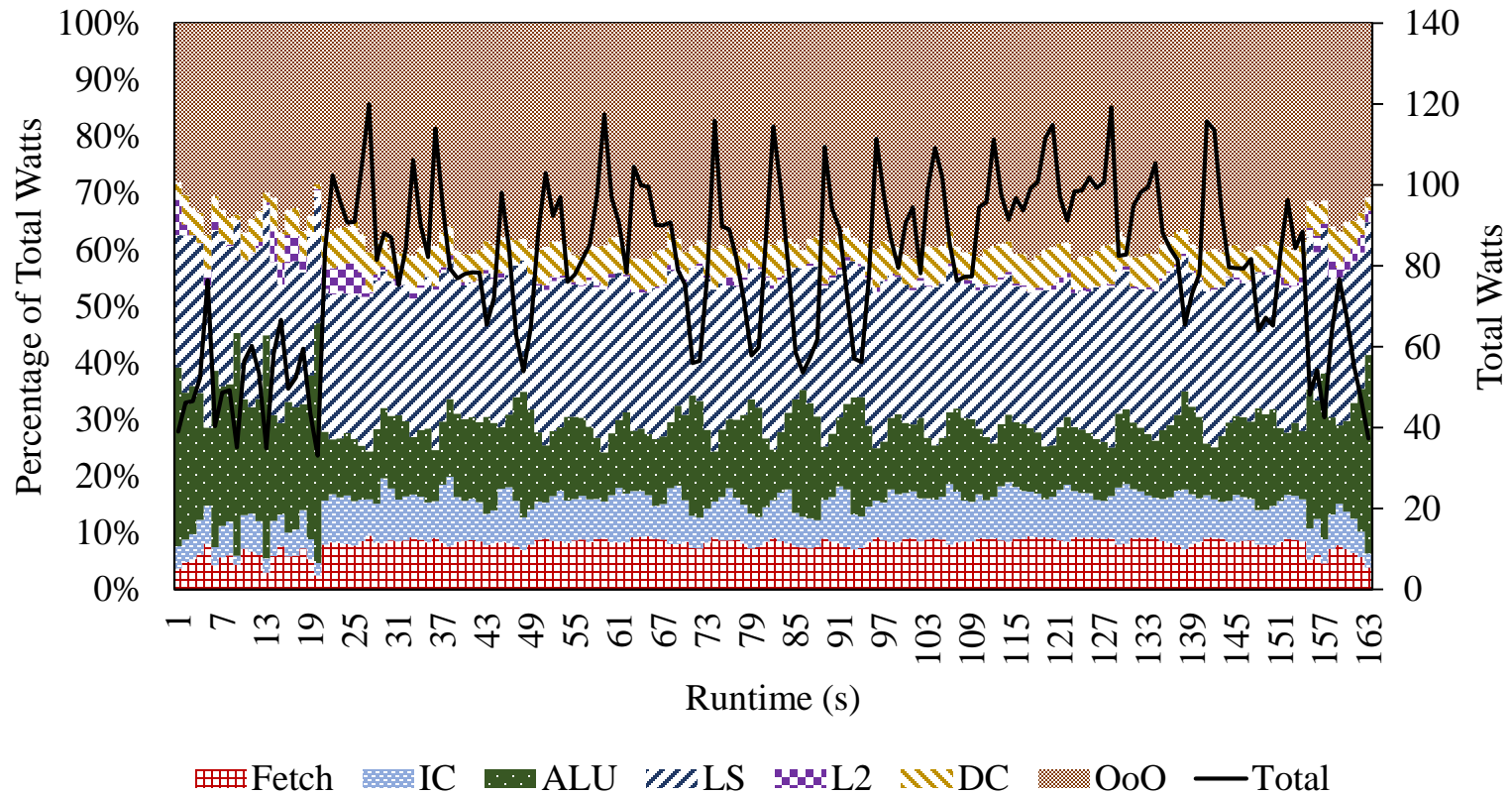
- **anneal workload (PARSEC)**
- **Per core and aggregate breakdown**
- **RAPL cannot provide core level breakdown**

Case Studies 2: Big Data Workloads



■ Big Data Workloads: Hadoop

Case Studies 3: Functional Unit Breakdowns



■ Word count power breakdown

Conclusion

- **WattWatcher fills an important role in power estimation techniques**
 - Real time results on real hardware
 - Highly configurable models
 - Minimal calibration required
 - Verified over different processors and vendors
 - MAPE = 2.67% averaged over all benchmarks
 - Illustrated over a number of interesting case studies

Thank you!

References

- [1] R. Ge *et al.*, “Powerpack: Energy profiling and analysis of high performance systems and applications,” *IEEE Transactions on Parallel and Distributed Systems*, vol. 21, no. 5, pp. 658–671, May 2010.
- [2] W. Bircher and L. John, “Complete system power estimation: A trickledown approach based on performance events,” in *ISPASS*, April 2007, pp. 158–168.
- [3] G. Contreras and M. Martonosi, “Power prediction for intel xscale processors using performance monitoring unit events,” in *ISLPED ’05*, 2005.
- [4] S. Gurumurthi *et al.*, “Using complete machine simulation for software power estimation: The softwatt approach,” in *HPCA*, 2002.
- [5] C. Isci and M. Martonosi, “Runtime power monitoring in high-end processors: methodology and empirical data,” in *MICRO*, Dec 2003, pp. 93–104.
- [6] R. Joseph and M. Martonosi, “Run-time power estimation in high performance microprocessors,” in *ISLPED 2001*
- [7] “AMD BIOS and Kernel Developer’s Guide for AMD family 15h Models 00h-0Fh Processors,” <http://support.amd.com/TechDocs/>.
- [8] J. Dongarra *et al.*, “Energy footprint of advanced dense numerical linear algebra using tile algorithms on multicore architectures,” in *CGC*, Nov 2012, pp. 274–281.
- [9] D. Brooks, V. Tiwari, and M. Martonosi, “Wattch: a framework for architectural-level power analysis and optimizations,” in *ISCA*, June 2000, pp. 83–94.
- [10] S. Li *et al.*, “Mcpat: An integrated power, area, and timing modeling framework for multicore and manycore architectures,” in *MICRO*, Dec 2009, pp. 469–480
- [11] J. L. Henning, “Spec cpu2006 benchmark descriptions,” SIGARCH Comput. Archit. News.
- [12] C. Bienia, “Benchmarking modern multiprocessors,” Ph.D. dissertation, Princeton University, January 2011.
- [13] W. Lee, et. al. “PowerTrain: A Learning-based Calibration of McPAT Power Models ,” in *ISLPED 2015*